

ABSTRACT

A chip card having a chip card body, a semiconductor chip and a carrier substrate, the carrier substrate being provided on both sides with surface contacts and with contact-hole lines electrically connecting the upper and lower surface contacts to one another. The contact-hole lines are arranged so close to the edge of the carrier substrate that their lower ends open out into a base of an outer cavity, where they are closed off, to thereby prevent moisture from penetrating into the inner cavity, in which the semiconductor chip is located, without it being necessary to produce a dedicated cover for the contact-hole lines.